

60V N-Channel MOSFET

Product Summary

$V_{(BR)DSS}$	$R_{DS(on)}TYP$	I_D
60V	26mΩ@10V	20A

Feature

- High density cell design for ultra low Rdson
- Fully characterized avalanche voltage and current
- Good stability and uniformity with high E_{AS}
- Excellent package for good heat dissipation
- AEC-Q101 qualified (Automotive grade with suffix "Q".)
- Exsemi technology

Application

- Power switching application
- Hard switched and high frequency circuits
- Uninterruptible Power Supply

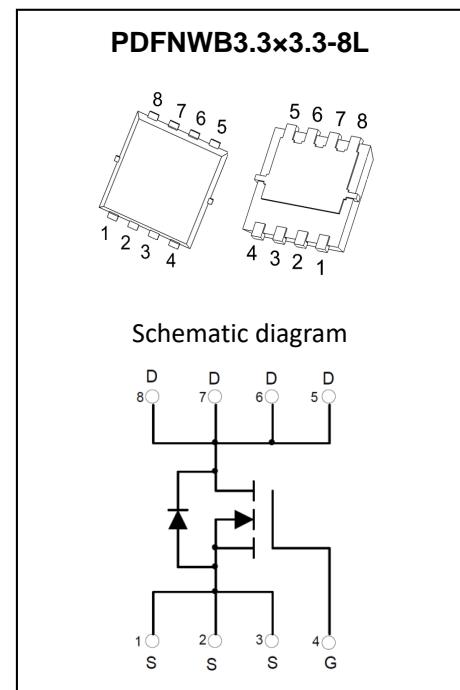
MARKING:



20N06 = Device code

Solid dot = Pin1 indicator

XX = Date Code



ABSOLUTE MAXIMUM RATINGS ($T_c=25^\circ\text{C}$ unless otherwise noted)

Parameter	Symbol	Value	Unit
Drain-Source Voltage	V_{DS}	60	V
Gate-Source Voltage	V_{GS}	± 20	V
Continuous Drain Current	I_D	20	A
Pulsed Drain Current	I_{DM}	60	A
Single Pulse Avalanche Energy ⁵	E_{AS}	70	mJ
Total Power Dissipation	P_D	1.5	W
Thermal Resistance from Junction to Ambient	$R_{\theta JA}$	83.3	°C/W
Junction Temperature	T_J	150	°C
Storage Temperature	T_{STG}	-55~+150	°C

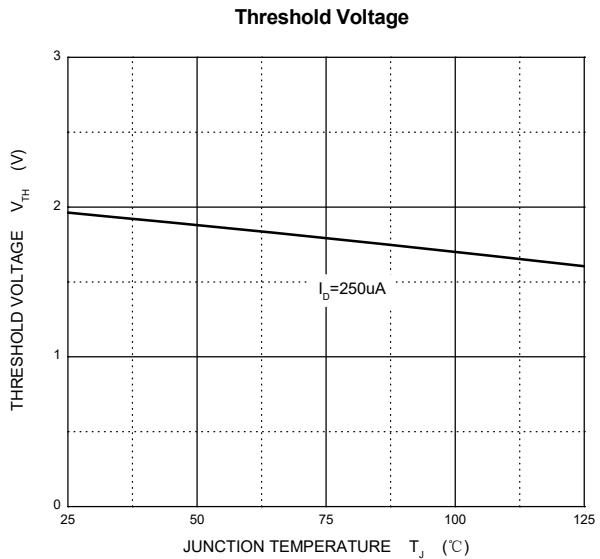
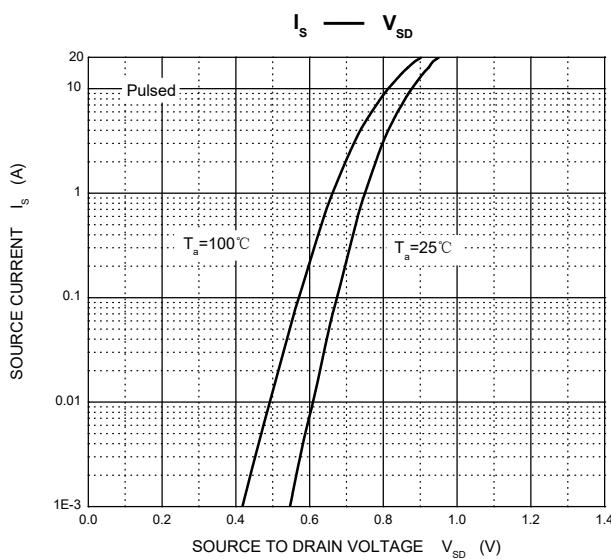
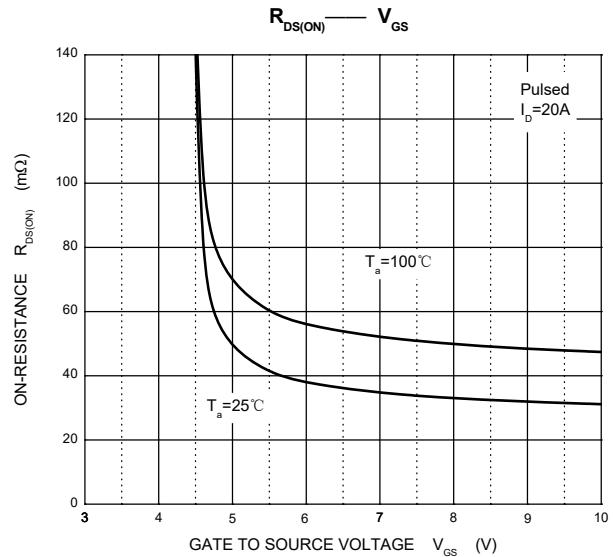
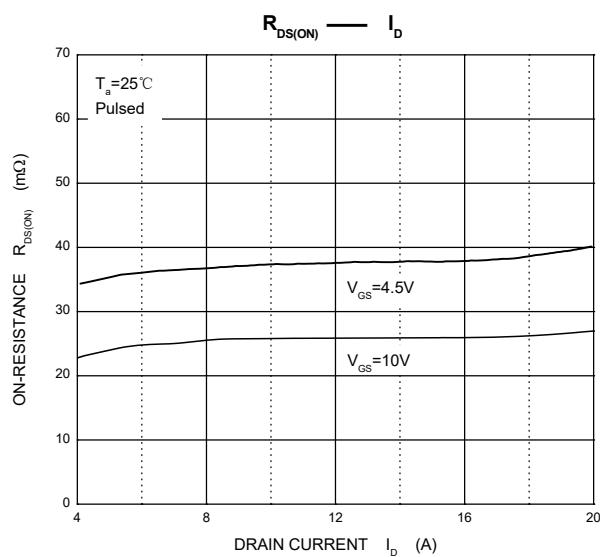
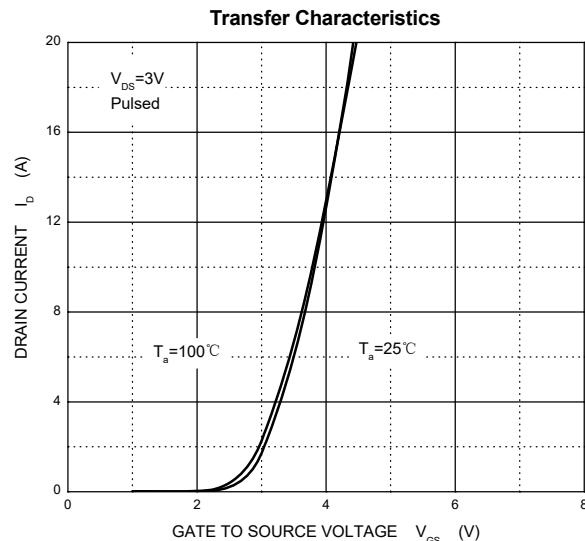
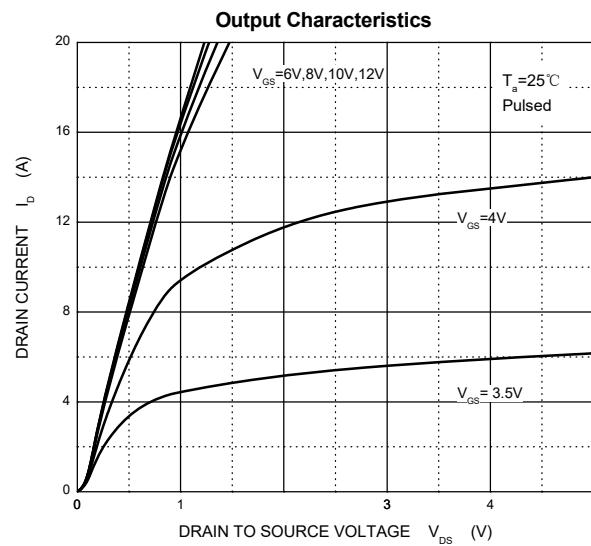
MOSFET ELECTRICAL CHARACTERISTICS($T_c=25^\circ\text{C}$ unless otherwise noted)

Parameter	Symbol	Test Condition	Min	Type	Max	Unit
Static Characteristics						
Drain-source breakdown voltage	$V_{(\text{BR})\text{DSS}}$	$V_{\text{GS}} = 0\text{V}, I_D = 250\mu\text{A}$	60			V
Zero gate voltage drain current	I_{DSS}	$V_{\text{DS}} = 60\text{V}, V_{\text{GS}} = 0\text{V}$			1	μA
Gate-body leakage current	I_{GSS}	$V_{\text{GS}} = \pm 20\text{V}, V_{\text{DS}} = 0\text{V}$			± 100	nA
Gate threshold voltage ³	$V_{\text{GS}(\text{th})}$	$V_{\text{DS}} = V_{\text{GS}}, I_D = 250\mu\text{A}$	1	2	3	V
Drain-source on-resistance ³	$R_{\text{DS}(\text{on})}$	$V_{\text{GS}} = 10\text{V}, I_D = 10\text{A}$		26	35	$\text{m}\Omega$
Forward transconductance ³	g_{FS}	$V_{\text{DS}} = 6\text{V}, I_D = 10\text{A}$	18			S
Dynamic characteristics⁴						
Input Capacitance	C_{iss}	$V_{\text{DS}} = 30\text{V}, V_{\text{GS}} = 0\text{V}, f = 1\text{MHz}$		960		pF
Output Capacitance	C_{oss}			62		
Reverse Transfer Capacitance	C_{rss}			54		
Switching Characteristics⁴						
Total Gate Charge @ -4.5V	Q_g	$V_{\text{DS}} = 48\text{V}, V_{\text{GS}} = 10\text{V}, I_D = 15\text{A}$		12		nC
Gate-Source Charge	Q_{gs}			4.1		
Gate-Drain Charge	Q_{gd}			4.5		
Turn-on delay time	$t_{\text{d}(\text{on})}$	$V_{\text{DD}} = 30\text{V}, V_{\text{GS}} = 10\text{V}, R_L = 15\Omega, R_G = 2.5\Omega$		5		ns
Turn-on rise time	t_r			2.6		
Turn-off delay time	$t_{\text{d}(\text{off})}$			17		
Turn-off fall time	t_f			2.5		
Diode Characteristics						
Continuous Source Current ²	I_s	$V_G = V_D = 0\text{V}$, Force Current			20	A
Diode Forward Voltage ³	V_{SD}	$V_{\text{GS}} = 0\text{V}, I_s = 1\text{A}, T_J = 25^\circ\text{C}$		0.72	1.2	V

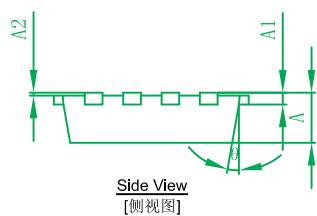
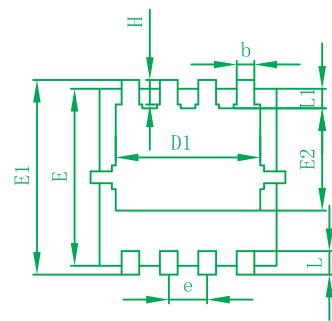
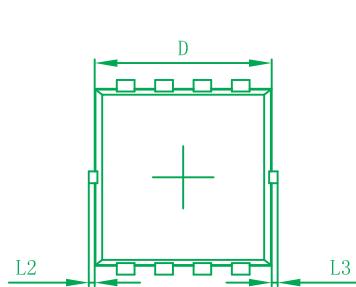
Note :

1. Repetitive Rating: Pulse width limited by maximum junction temperature.
2. Surface Mounted on FR4 Board, $t \leq 10$ sec.
3. Pulse Test: Pulse Width $\leq 300\mu\text{s}$, Duty Cycle $\leq 2\%$.
4. Guaranteed by design, not subject to production
5. EAS condition : $T_J = 25^\circ\text{C}, V_{\text{DD}} = 30\text{V}, V_G = 10\text{V}, L = 0.5\text{mH}, R_g = 25\Omega$

Typical Electrical and Thermal Characteristics



PDFNWB3.3*3.3-8L Package Information

Top View
[顶视图]Bottom View
[背视图]

Symbol	Dimensions In Millimeters		Dimensions In Inches	
	Min.	Max.	Min.	Max.
A	0.650	0.850	0.026	0.033
A1	0.152 REF.		0.006 REF.	
A2	0~0.05		0~0.002	
D	2.900	3.100	0.114	0.122
D1	2.300	2.600	0.091	0.102
E	2.900	3.100	0.114	0.122
E1	3.150	3.450	0.124	0.136
E2	1.535	1.935	0.060	0.076
b	0.200	0.400	0.008	0.016
e	0.550	0.750	0.022	0.030
L	0.300	0.500	0.012	0.020
L1	0.180	0.480	0.007	0.019
L2	0~0.100		0~0.004	
L3	0~0.100		0~0.004	
H	0.315	0.515	0.012	0.020
θ	9°	13°	9°	13°

Device	Package	Shipping
EP20N06P33	PDFNWB3.3*3.3-8L	5000/Tape&Reel