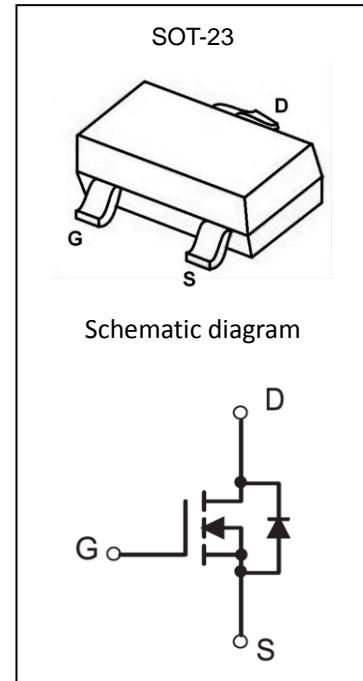


50V N-Channel MOSFET

Product Summary

$V_{(BR)DSS}$	$R_{DS(on)MAX}$	I_D
50V	3.5Ω@10V	0.22A
	6Ω@4.5V	



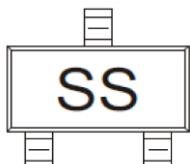
Feature

- High density cell design for extremely low $R_{DS(on)}$
- Rugged and Reliable
- AEC-Q101 qualified (Automotive grade with suffix "Q".)
- Exsemi technology

Application

- Direct Logic-Level Interface: TTL/CMOS
- Drivers: Relays, Solenoids, Lamps, Hammers, Display, Memories, Transistors, etc.
- Battery Operated Systems
- Solid-State Relays

MARKING:

ABSOLUTE MAXIMUM RATINGS ($T_a=25^\circ\text{C}$ unless otherwise noted)

Parameter	Symbol	Value	Unit
Drain-Source Voltage	V_{DS}	50	V
Gate-Source Voltage	V_{GS}	± 20	
Continuous Drain Current	I_D	0.22	A
Power Dissipation	P_D	0.35	W
Thermal Resistance from Junction to Ambient	$R_{\theta JA}$	357	$^\circ\text{C}/\text{W}$
Junction Temperature	T_J	150	$^\circ\text{C}$
Storage Temperature	T_{STG}	-55~+150	

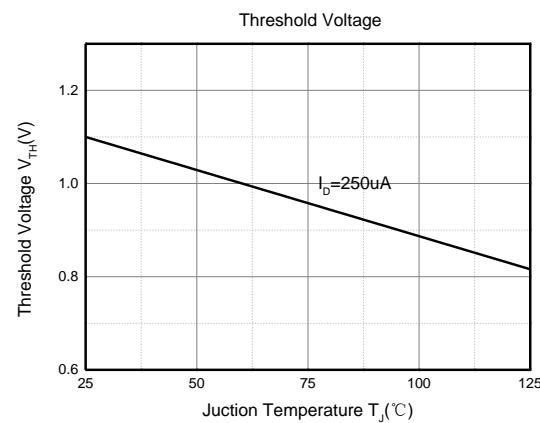
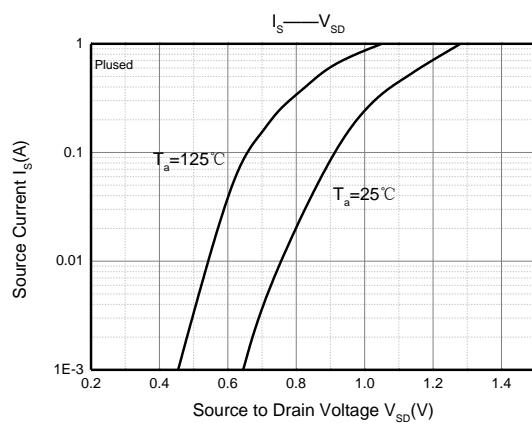
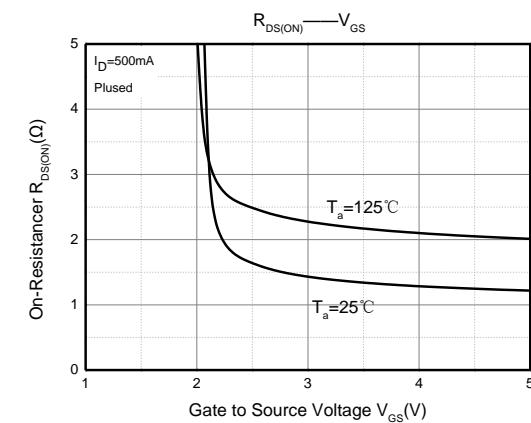
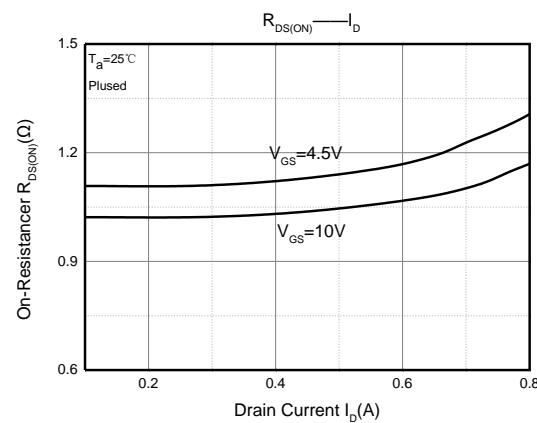
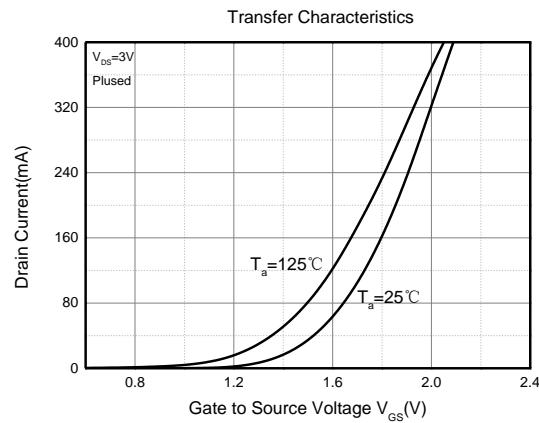
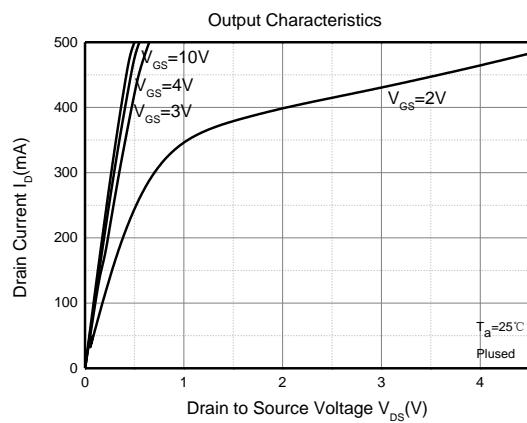
MOSFET ELECTRICAL CHARACTERISTICS($T_a=25^\circ\text{C}$ unless otherwise noted)

Parameter	Symbol	Test Condition	Min	Type	Max	Unit
Static Characteristics						
Drain-source breakdown voltage	$V_{(\text{BR})\text{DSS}}$	$V_{\text{GS}} = 0\text{V}, I_D = 250\mu\text{A}$	50			V
Zero gate voltage drain current	$I_{\text{DSS}1}$	$V_{\text{DS}} = 50\text{V}, V_{\text{GS}} = 0\text{V}$			0.5	μA
	$I_{\text{DSS}2}$	$V_{\text{DS}} = 30\text{V}, V_{\text{GS}} = 0\text{V}$			100	nA
Gate-body leakage current	I_{GSS}	$V_{\text{GS}} = \pm 20\text{V}, V_{\text{DS}} = 0\text{V}$			± 100	nA
Gate threshold voltage ¹	$V_{\text{GS}(\text{th})}$	$V_{\text{DS}} = V_{\text{GS}}, I_D = 250\mu\text{A}$	0.8	1.1	1.5	V
Drain-source on-resistance ¹	$R_{\text{DS}(\text{on})}$	$V_{\text{GS}} = 10\text{V}, I_D = 0.22\text{A}$		1.0	3.5	Ω
		$V_{\text{GS}} = 4.5\text{V}, I_D = 0.22\text{A}$		1.1	6.0	
Forward transconductance ¹	g_{FS}	$V_{\text{DS}} = 10\text{V}, I_D = 0.22\text{A}$		0.13		S
Dynamic characteristics²						
Input Capacitance	C_{iss}	$V_{\text{DS}} = 25\text{V}, V_{\text{GS}} = 0\text{V}, f = 1\text{MHz}$		26.5		pF
Output Capacitance	C_{oss}			12.9		
Reverse Transfer Capacitance	C_{rss}			5.9		
Switching Characteristics^{1,2}						
Turn-on delay time	$t_{\text{d}(\text{on})}$	$V_{\text{DD}} = 30\text{V}, I_D = 0.29\text{A}, V_{\text{GS}} = 10\text{V}, R_G = 6\Omega$			5	nS
Turn-on rise time	t_r				18	
Turn-off delay time	$t_{\text{d}(\text{off})}$				36	
Turn-off fall time	t_f				14	
Source-Drain Diode characteristics¹						
Diode Forward voltage	V_{SD}	$I_S = 0.44\text{A}, V_{\text{GS}} = 0\text{V}$		1.15	1.4	V

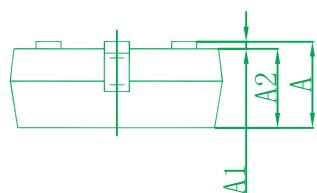
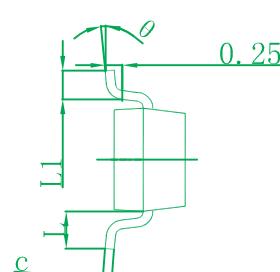
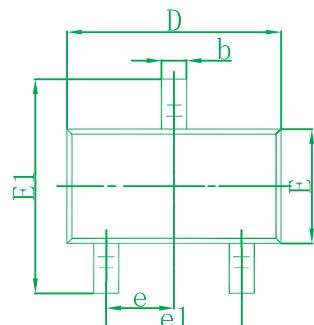
Notes:

1. Pulse Test ; Pulse Width $\leq 300\mu\text{s}$, Duty Cycle $\leq 2\%$.
2. These parameters have no way to verify.

Typical Electrical and Thermal Characteristics

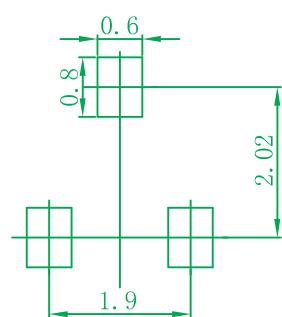


SOT-23 Package Information



Symbol	Dimensions In Millimeters		Dimensions In Inches	
	Min	Max	Min	Max
A	0.900	1.150	0.035	0.045
A1	0.000	0.100	0.000	0.004
A2	0.900	1.150	0.035	0.045
b	0.300	0.500	0.012	0.020
c	0.080	0.150	0.003	0.006
D	2.800	3.050	0.110	0.120
E	1.200	1.400	0.047	0.055
E1	2.250	2.550	0.089	0.100
e	0.950 TYP		0.037 TYP	
e1	1.800	2.000	0.071	0.079
L	0.550 REF		0.022 REF	
L1	0.360 REF		0.014 REF	
θ	0°	8°	0°	8°

SOT-23 Suggested Pad Layout



Note:

1. Controlling dimension: in millimeters.
2. General tolerance: $\pm 0.05\text{mm}$.
3. The pad layout is for reference purposes only.

Ordering information

Device	Package	Shipping
BSS138	SOT-23	3000/Tape&Reel(7inches)